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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/802,020	03/08/2001	Ashley Saulsbury	016747015210	4703
20350	7590	07/20/2004		
TOWNSEND AND TOWNSEND AND CREW, LLP TWO EMBARCADERO CENTER EIGHTH FLOOR SAN FRANCISCO, CA 94111-3834			EXAMINER DO, CHAT C	
			ART UNIT 2124	PAPER NUMBER

DATE MAILED: 07/20/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Advisory Action</b>	<b>Application No.</b>	<b>Applicant(s)</b>
	09/802,020	SAULSBURY ET AL.
	<b>Examiner</b>	<b>Art Unit</b>
	Chat C. Do	2124

--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED 19 May 2004 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE. Therefore, further action by the applicant is required to avoid abandonment of this application. A proper reply to a final rejection under 37 CFR 1.113 may only be either: (1) a timely filed amendment which places the application in condition for allowance; (2) a timely filed Notice of Appeal (with appeal fee); or (3) a timely filed Request for Continued Examination (RCE) in compliance with 37 CFR 1.114.

**PERIOD FOR REPLY** [check either a) or b)]

- a)  The period for reply expires \_\_\_\_\_ months from the mailing date of the final rejection.
- b)  The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.  
ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

- 1.  A Notice of Appeal was filed on \_\_\_\_\_. Appellant's Brief must be filed within the period set forth in 37 CFR 1.192(a), or any extension thereof (37 CFR 1.191(d)), to avoid dismissal of the appeal.
- 2.  The proposed amendment(s) will not be entered because:
  - (a)  they raise new issues that would require further consideration and/or search (see NOTE below);
  - (b)  they raise the issue of new matter (see Note below);
  - (c)  they are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or
  - (d)  they present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: \_\_\_\_\_

- 3.  Applicant's reply has overcome the following rejection(s): \_\_\_\_\_.
- 4.  Newly proposed or amended claim(s) \_\_\_\_\_ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).
- 5.  The a) affidavit, b) exhibit, or c) request for reconsideration has been considered but does NOT place the application in condition for allowance because: See below.
- 6.  The affidavit or exhibit will NOT be considered because it is not directed SOLELY to issues which were newly raised by the Examiner in the final rejection.
- 7.  For purposes of Appeal, the proposed amendment(s) a) will not be entered or b) will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.

The status of the claim(s) is (or will be) as follows:

Claim(s) allowed: \_\_\_\_\_

Claim(s) objected to: \_\_\_\_\_

Claim(s) rejected: 1-18 and 20-22.

Claim(s) withdrawn from consideration: \_\_\_\_\_

- 8.  The drawing correction filed on \_\_\_\_\_ is a) approved or b) disapproved by the Examiner.
- 9.  Note the attached Information Disclosure Statement(s)( PTO-1449) Paper No(s). \_\_\_\_\_.
- 10.  Other: \_\_\_\_\_

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Part 5(c): the applicant arguments have been fully considered by the examiner but they are not persuasive, for instance a single instruction can be a single instruction or an instruction with multiple-sub instructions. Therefore the examiner stands firmly on the rejections made on last Office action wherein Sidwell et al. disclose in Figures 7, 10, and 15 a method for processing a matrix of elements in a processor (abstract discloses the processes of transposing the matrix), the method comprising steps of: loading a first subset of matrix elements from a first location (Annexe A Sequence (ii) in cols. 15-16 wherein Figure 10 illustrates the zip instruction as reading from SRC1 to 170 for first subset of matrix elements); loading a second subset of matrix elements from a second location (Annexe A Sequence (ii) in cols. 15-16 wherein Figure 10 illustrates the zip instruction as reading from SRC2 to 172 for second subset of matrix elements); storing a third subset of matrix elements in a first destination (Annexe A Sequence (ii) in cols. 15-16 wherein Figure 10 illustrates the zip instruction as storing into 176 for first final transpose matrix as seen in Figure 15 bottom right matrix); and storing a fourth subset of matrix elements in a second destination (Annexe A Sequence (ii) in cols. 15-16 wherein Figure 10 illustrates the zip instruction as storing into 174 for second final transpose matrix as seen in Figure 15 bottom right matrix), wherein the loading and storing steps result from a first instruction issue (160 OPCODE and Zip Instruction of 192) at least one of the first and second destination consists of a row or column of a second matrix (Figure 15 bottom right matrix e.g. R1 = {A0 B0 C0 D0} is a transposed of {A0; B0; C0; D0}) and the second matrix corresponds to a transposition of the matrix (each row or register in Figure 15 bottom right matrix is an transposed vector of the original matrix upper left).



JOHN CHAVIS  
PATENT EXAMINER  
ART UNIT 2124

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